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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Lasserre

Serial No.: 09/932,381

Filed: August 17, 2001

Art Unit: 2188 Examiner: Bragdon

Docket Number: Ti-31354

Local Memory With Indicator Bits to Support Concurrent DMA and

CPU Access

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December 7, 2004

FACSIMILE COVER SHEET

| X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION ASSIGNMENT FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N DIVISIONAL APP'N | X AMENDMENT UNDER RULE 116 (11 Pages) X FEE TRANSMITTAL (1 Page) NOTICE OF APPEAL APPEAL ISSUE FEE |
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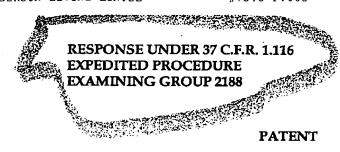
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| Effective on 10/01/2004. Patent fees are subject to annual revision. | Complete if Known | | |
| | Application Number 09/932,381 PECF | | |
| FEE TRANSMITTAL | Filing Date | August 17,2001 PENTRALE | |
| For FY 2005 | First Named Inventor | Lasserre | |
| Applicant claims small entity status. See 37 CFR 1.27 | Examiner Name | Bragdon DEC 0 | |
| | Art Unit | 2188 | |
| TOTAL AMOUNT OF PAYMENT (\$) 0 | Attorney Docket No. | TI-31354 | |
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| | Non-English specification | 130 130 | |
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| SUBMITTED BY | | | |
| | egistration No. 31,939: | Telephone 972.664.9554 | |
| Name (Print/Type Rodney M. Anderson | | Date 7 Dec 2004 | |

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In re the Application of:

Serge Lasserre (TI-31354)

Conf. No.: 4434

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Art Unit: 2188

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

AMENDMENT UNDER RULE 116

Introductory Comments:

This paper is presented in response to the Office Action mailed on October 7, 2004. Entry of this amendment in this application, and reconsideration of the final rejection of claims in this application, are respectfully requested.

Proposed amendment to the claims begin on page 2.

Applicant's remarks begin on page 8.